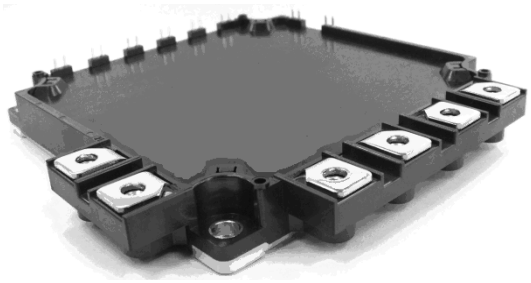
MITSUBISHI ELECTRIC

<IGBT Modules>

CM300RXL-24S1

HIGH POWER SWITCHING USE
INSULATED TYPE



sevenpack (3φ Inverter+Chopper Brake)

Collector current I_C ..... **3 0 0** A
Collector-emitter voltage V_{CES} ..... **1 2 0 0** V
Maximum junction temperature T_{jmax} ..... **1 7 5** °C

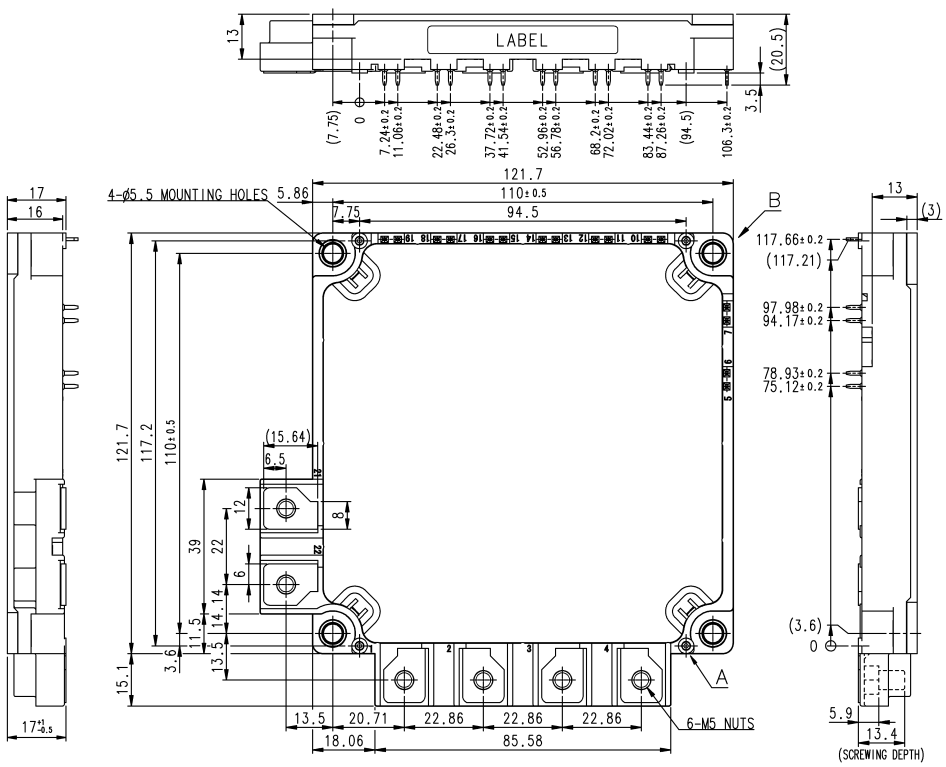
- ●Flat base Type
- ●Copper base plate (non-plating)
- ●Tin plating pin terminals
- ●RoHS Directive compliant
- ●Recognized under UL1557, File E323585

APPLICATION

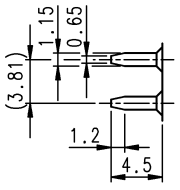
AC Motor Control, Motion/Servo Control, Power supply, etc.

OUTLINE DRAWING & INTERNAL CONNECTION

Dimension in mm

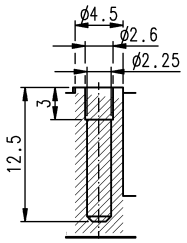


TERMINAL

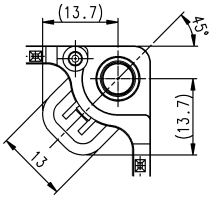


t=0.8

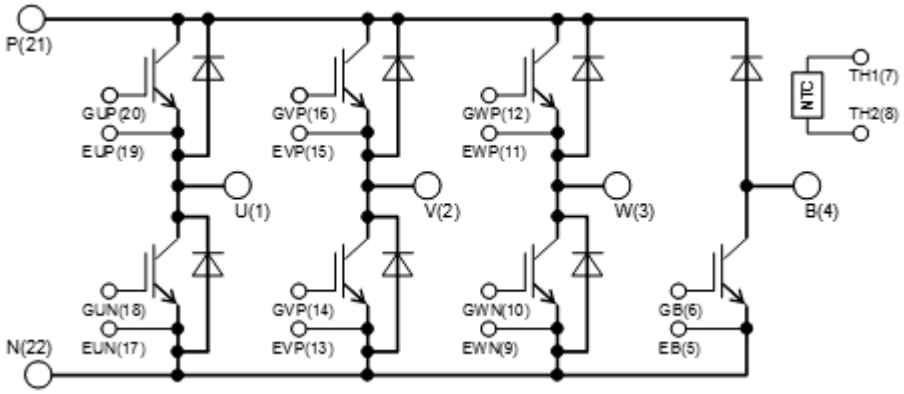
SECTION A



DETAIL B



INTERNAL CONNECTION



Tolerance otherwise specified

Division of Dimension		Tolerance
0.5	to 3	±0.2
over 3	to 6	±0.3
over 6	to 30	±0.5
over 30	to 120	±0.8
over 120	to 400	±1.2

MAXIMUM RATINGS (T_j=25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Item	Conditions	Rating	Unit
V_{CES}	Collector-emitter voltage	G-E short-circuited	1200	V
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
I_C	Collector current	DC, T_C=94 °C (Note2, 4)	300	A
I_{CRM}		Pulse, Repetitive, V_{GE}=15 V (Note3)	600	
P_{tot}	Total power dissipation	T_C=25 °C (Note2, 4)	1665	W
I_E (Note1)	Emitter current	DC (Note2)	300	A
I_{ERM} (Note1)		Pulse, Repetitive (Note3)	600	

BRAKE PART IGBT/DIODE

Symbol	Item	Conditions	Rating	Unit
V_{CES}	Collector-emitter voltage	G-E short-circuited	1200	V
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
I_C	Collector current	DC, T_C=107 °C (Note2, 4)	150	A
I_{CRM}		Pulse, Repetitive (Note3)	300	
P_{tot}	Total power dissipation	T_C=25 °C (Note2, 4)	935	W
V_{RRM}	Repetitive peak reverse voltage	G-E short-circuited	1200	V
I_F	Forward current	DC (Note2)	150	A
I_{FRM}		Pulse, Repetitive (Note3)	300	

MODULE

Symbol	Item	Conditions	Rating	Unit
V_{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T_{jmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C
T_{Cmax}	Maximum case temperature	(Note4)	125	
T_{jop}	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T_{stg}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (T_j=25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
I_{CES}	Collector-emitter cut-off current	V_{CE}=V_{CES}, G-E short-circuited		-	-	1.0	mA
I_{GES}	Gate-emitter leakage current	V_{GE}=V_{GES}, C-E short-circuited		-	-	0.5	μA
$V_{GE(th)}$	Gate-emitter threshold voltage	I_C=30 mA, V_{CE}=10 V		5.4	6.0	6.6	V
V_{CEsat} (Terminal)	Collector-emitter saturation voltage	I_C=300 A, V_{GE}=15 V, Refer to the figure of test circuit (Note5)	T_j=25 °C	-	2.00	2.45	V
			T_j=125 °C	-	2.30	-	
			T_j=150 °C	-	2.40	-	
V_{CEsat} (Chip)		I_C=300 A, V_{GE}=15 V, (Note5)	T_j=25 °C	-	1.85	2.35	V
			T_j=125 °C	-	2.10	-	
			T_j=150 °C	-	2.15	-	
C_{ies}	Input capacitance	V_{CE}=10 V, G-E short-circuited		-	-	25	nF
C_{oes}	Output capacitance			-	-	5.0	
C_{res}	Reverse transfer capacitance			-	-	0.42	
Q_G	Gate charge	V_{CC}=600 V, I_C=300 A, V_{GE}=15 V		-	525	-	nC
$t_{d(on)}$	Turn-on delay time	V_{CC}=600 V, I_C=300 A, V_{GE}=±15 V, R_G=0 Ω, Inductive load		-	-	800	ns
t_r	Rise time			-	-	200	
$t_{d(off)}$	Turn-off delay time			-	-	600	
t_f	Fall time			-	-	300	

ELECTRICAL CHARACTERISTICS (cont.; T_j=25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
V_{EC} (Note.1) (Terminal)	Emitter-collector voltage	I_E=300 A, G-E short-circuited, Refer to the figure of test circuit (Note5)	T_j=25 °C	-	2.80	3.60	V
			T_j=125 °C	-	2.40	-	
			T_j=150 °C	-	2.30	-	
V_{EC} (Note.1) (Chip)		I_E=300 A, G-E short-circuited, (Note5)	T_j=25 °C	-	2.70	3.50	V
			T_j=125 °C	-	2.30	-	
			T_j=150 °C	-	2.20	-	
t_{rr} (Note1)	Reverse recovery time	V_{CC}=600 V, I_E=300 A, V_{GE}=±15 V, R_G=0 Ω, Inductive load		-	-	300	ns
Q_{rr} (Note1)	Reverse recovery charge			-	8.0	-	µC
E_{on}	Turn-on switching energy per pulse	V_{CC}=600 V, I_C=I_E=300 A, V_{GE}=±15 V, R_G=0 Ω, T_j=150 °C, Inductive load		-	45.8	-	mJ
E_{off}	Turn-off switching energy per pulse			-	31.6	-	
E_{rr} (Note1)	Reverse recovery energy per pulse			-	18.1	-	mJ
$R_{CC'+EE'}$	Internal lead resistance	Main terminals-chip, per switch, T_C=25 °C (Note4)		-	-	1.5	mΩ
r_g	Internal gate resistance	Per switch		-	10	-	Ω

BRAKE PART IGBT/DIODE

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
I_{CES}	Collector-emitter cut-off current	V_{CE}=V_{CES}, G-E short-circuited		-	-	1.0	mA
I_{GES}	Gate-emitter leakage current	V_{GE}=V_{GES}, C-E short-circuited		-	-	0.5	µA
$V_{GE(th)}$	Gate-emitter threshold voltage	I_C=15 mA, V_{CE}=10 V		5.4	6.0	6.6	V
V_{CEsat} (Terminal)	Collector-emitter saturation voltage	I_C=150 A, V_{GE}=15 V, Refer to the figure of test circuit (Note5)	T_j=25 °C	-	1.80	2.25	V
			T_j=125 °C	-	2.00	-	
			T_j=150 °C	-	2.05	-	
V_{CEsat} (Chip)		I_C=150 A, V_{GE}=15 V, (Note5)	T_j=25 °C	-	1.70	2.15	V
			T_j=125 °C	-	1.90	-	
			T_j=150 °C	-	1.95	-	
C_{ies}	Input capacitance	V_{CE}=10 V, G-E short-circuited		-	-	15	nF
C_{oes}	Output capacitance			-	-	3.0	
C_{res}	Reverse transfer capacitance			-	-	0.25	
Q_G	Gate charge	V_{CC}=600 V, I_C=150 A, V_{GE}=15 V		-	315	-	nC
$t_{d(on)}$	Turn-on delay time	V_{CC}=600 V, I_C=150 A, V_{GE}=±15 V, R_G=0 Ω, Inductive load		-	-	800	ns
t_r	Rise time			-	-	200	
$t_{d(off)}$	Turn-off delay time			-	-	600	
t_f	Fall time			-	-	300	
I_{RRM}	Repetitive peak reverse current	V_R=V_{RRM}, G-E short-circuited		-	-	1.0	mA
V_F (Terminal)	Forward voltage	I_F=150 A, Refer to the figure of test circuit (Note5)	T_j=25 °C	-	2.60	3.40	V
			T_j=125 °C	-	2.16	-	
			T_j=150 °C	-	2.10	-	
V_F (Chip)		I_F=150 A, G-E short-circuited, (Note5)	T_j=25 °C	-	2.50	3.30	V
			T_j=125 °C	-	2.06	-	
			T_j=150 °C	-	2.00	-	
t_{rr}	Reverse recovery time	V_{CC}=600 V, I_E=150 A, V_{GE}=±15 V, R_G=0 Ω, Inductive load		-	-	300	ns
Q_{rr}	Reverse recovery charge			-	4.0	-	µC
E_{on}	Turn-on switching energy per pulse	V_{CC}=600 V, I_C=I_E=150 A, V_{GE}=±15 V, R_G=0 Ω, T_j=150 °C, Inductive load		-	16.6	-	mJ
E_{off}	Turn-off switching energy per pulse			-	17.6	-	
E_{rr}	Reverse recovery energy per pulse			-	10.8	-	mJ
r_g	Internal gate resistance	-		-	13	-	Ω

ELECTRICAL CHARACTERISTICS (cont.; T_j=25 °C, unless otherwise specified)

NTC THERMISTOR PART

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
R_{25}	Zero-power resistance	T_C=25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R_{100}=493 Ω, T_C=100 °C (Note4)	-7.3	-	+7.8	%
$B_{(25/50)}$	B-constant	Approximate by equation (Note6)	-	3375	-	K
P_{25}	Power dissipation	T_C=25 °C (Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	0.09	K/W
$R_{th(j-c)D}$		Junction to case, per Inverter FWD (Note4)	-	-	0.15	
$R_{th(j-c)Q}$		Junction to case, per Brake IGBT (Note4)	-	-	0.16	K/W
$R_{th(j-c)D}$		Junction to case, per Brake DIODE (Note4)	-	-	0.26	
$R_{th(c-s)}$	Contact thermal resistance	Case to heat sink, per 1 module, Thermal grease applied (Note4, 7)	-	7	-	K/kW

MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
M_t	Mounting torque	Main terminals M 5 screw	2.5	3.0	3.5	N·m
M_s	Mounting torque	Mounting to heat sink M 5 screw	2.5	3.0	3.5	N·m
d_s	Creepage distance	Terminal to terminal	16.3	-	-	mm
		Terminal to base plate	16.8	-	-	
d_a	Clearance	Terminal to terminal	10	-	-	mm
		Terminal to base plate	10	-	-	
m	mass	-	-	690	-	g
e_c	Flatness of base plate	On the centerline X, Y (Note8)	±0	-	+100	μm

*: This product is This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (DIODE).

2. Junction temperature (T_j) should not increase beyond T_{jmax} rating.

3. Pulse width and repetition rate should be such that the device junction temperature (T_j) dose not exceed T_{jmax} rating.

4. Case temperature (T_C) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.

5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

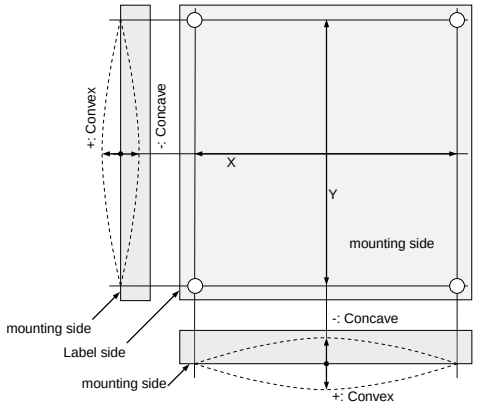
6. $B_{(25/50)} = \ln(\frac{R_{25}}{R_{50}}) / (\frac{1}{T_{25}} - \frac{1}{T_{50}})$

   R_{25}: resistance at absolute temperature T_{25} [K]; T_{25}=25 [°C]+273.15=298.15 [K]

   R_{50}: resistance at absolute temperature T_{50} [K]; T_{50}=50 [°C]+273.15=323.15 [K]

7. Typical value is measured by using thermally conductive grease of λ=0.9 W/(m·K).

8. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



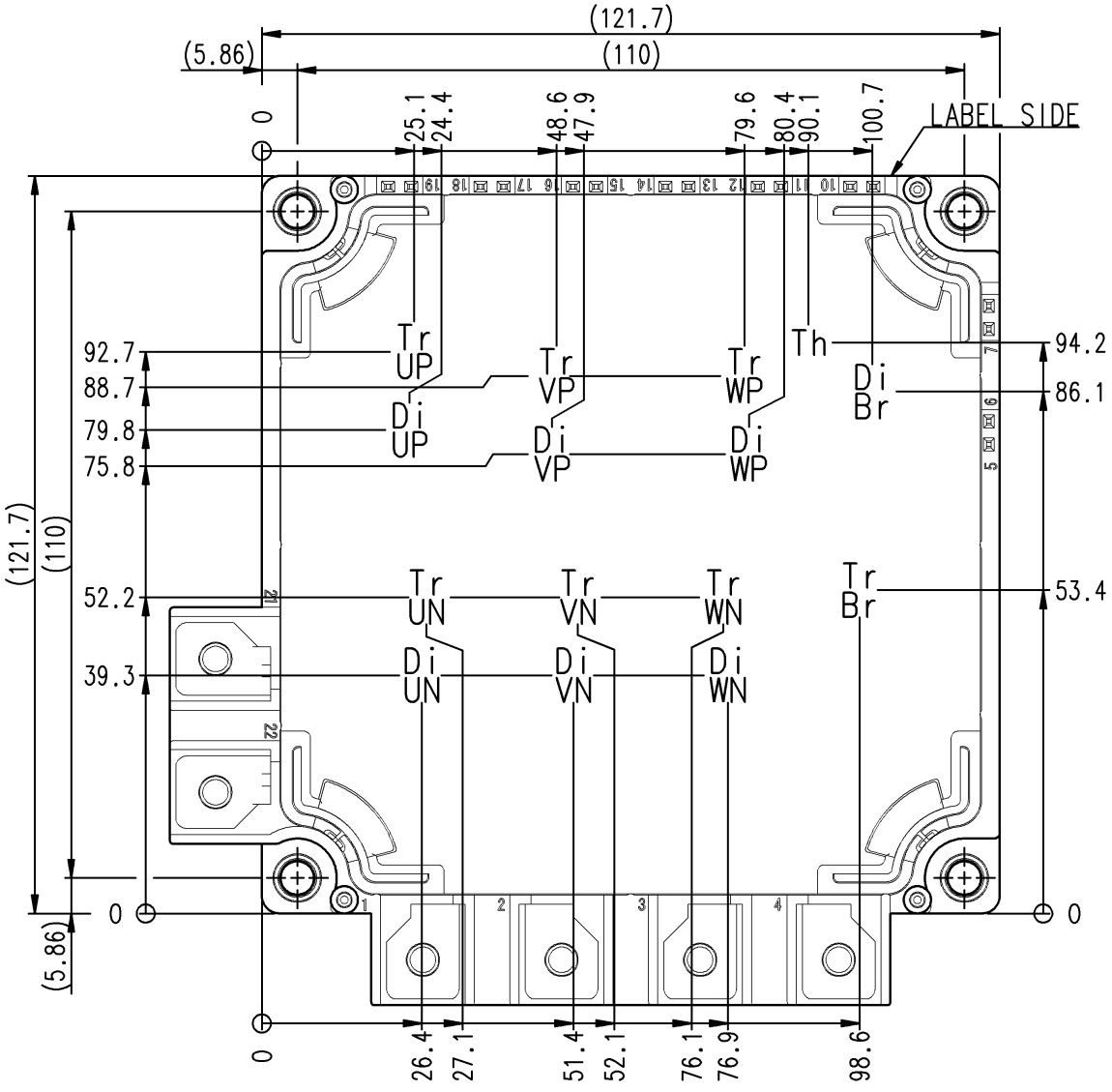
Note9. Use the following screws when mounting the printed circuit board (PCB) on the stand offs.
"φ2.6×10 or φ2.6×12 B1 tapping screw"
The length of the screw depends on thickness (t1.6~t2.0) of the PCB.

RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
V_{CC}	(DC) Supply voltage	Applied across P-N terminals		-	600	850	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across GB-EB terminals		13.5	15.0	16.5	V
		Applied across G*P-E*P/G*N-E*N(*=U, V, W) terminals		14.0	15.0	16.5	
R_G	External gate resistance	Per switch	Inverter IGBT	0	-	15	Ω
			Brake IGBT	0	-	30	

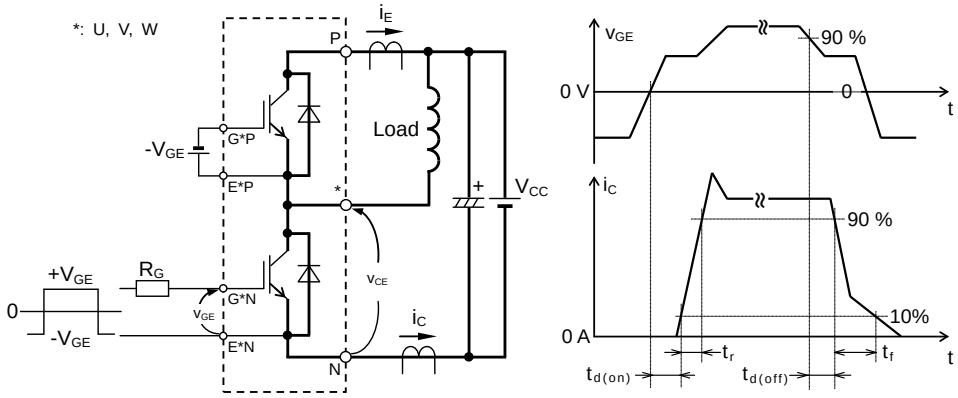
CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm

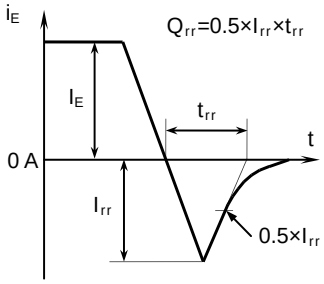


Tr*P/Tr*N/TrBr: IGBT, Di*P/Di*N: FWD (*=U/V/W), DiBr: BRAKE DIODE, Th: NTC thermistor

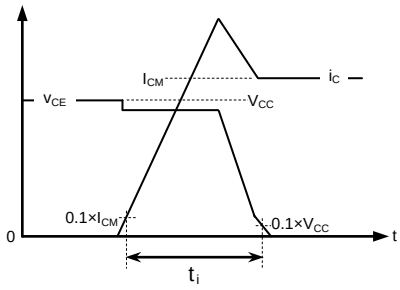
TEST CIRCUIT AND WAVEFORMS



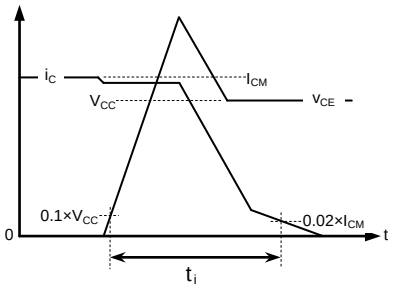
Switching characteristics test circuit and waveforms



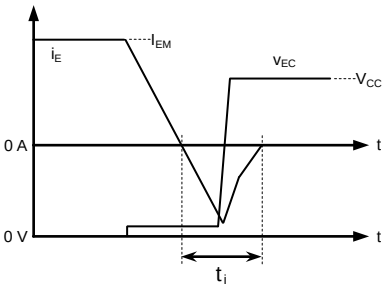
t_{rr}, Q_{rr} characteristics test waveform



IGBT Turn-on switching energy



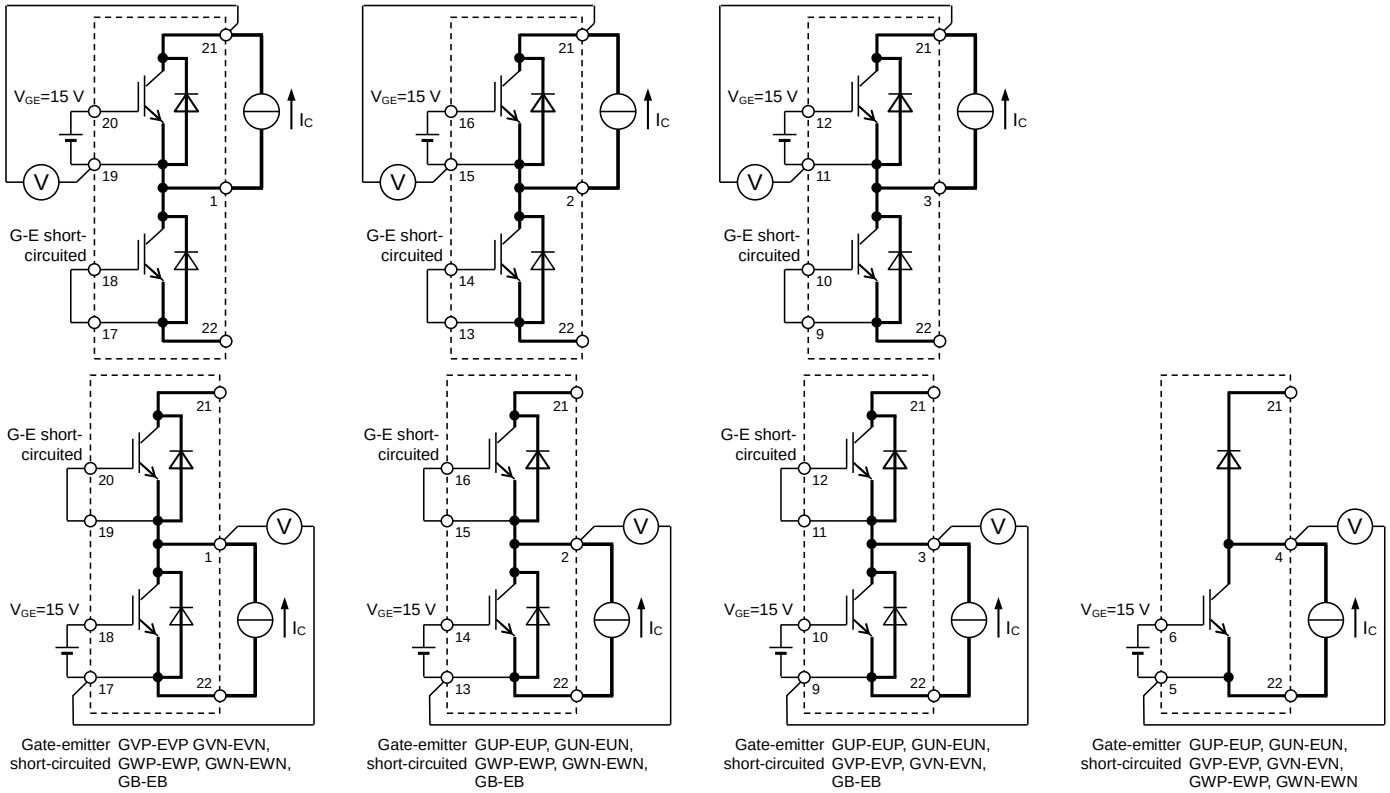
IGBT Turn-off switching energy



FWD/DIODE Reverse recovery energy

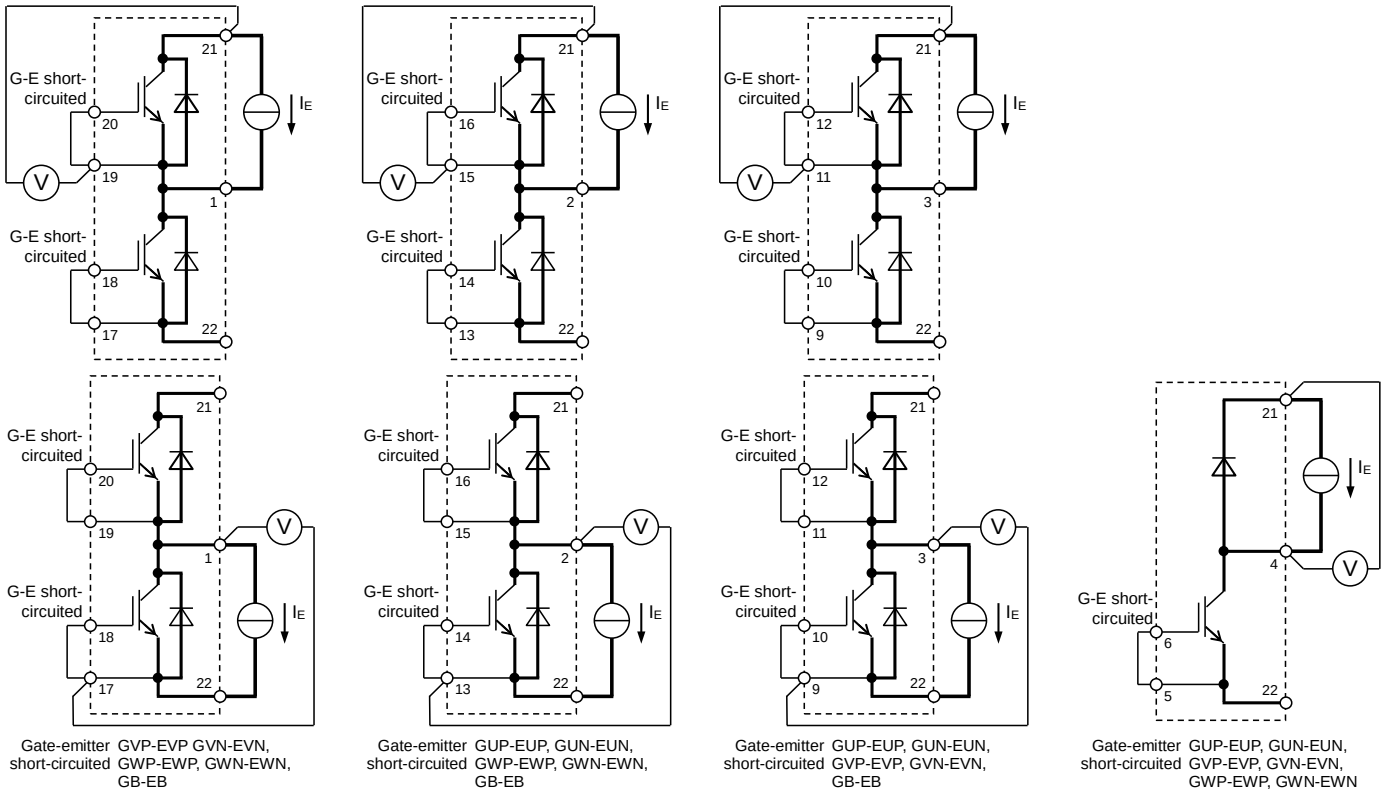
Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

TEST CIRCUIT



UP / UN IGBT　VP / VN IGBT　WP / WN IGBT　Brake IGBT

V_{CEsat} characteristics test circuit



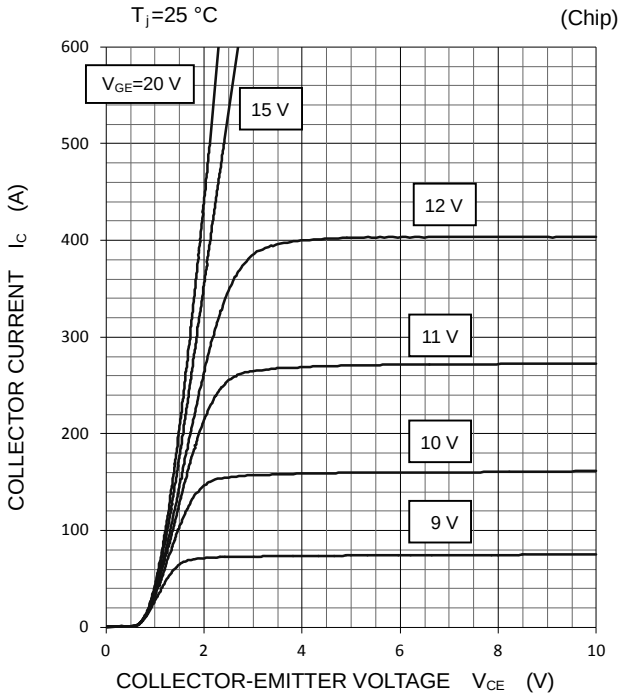
UP / UN FWD　VP / VN FWD　WP / WN FWD　Brake DIODE

V_{EC} / V_F characteristics test circuit

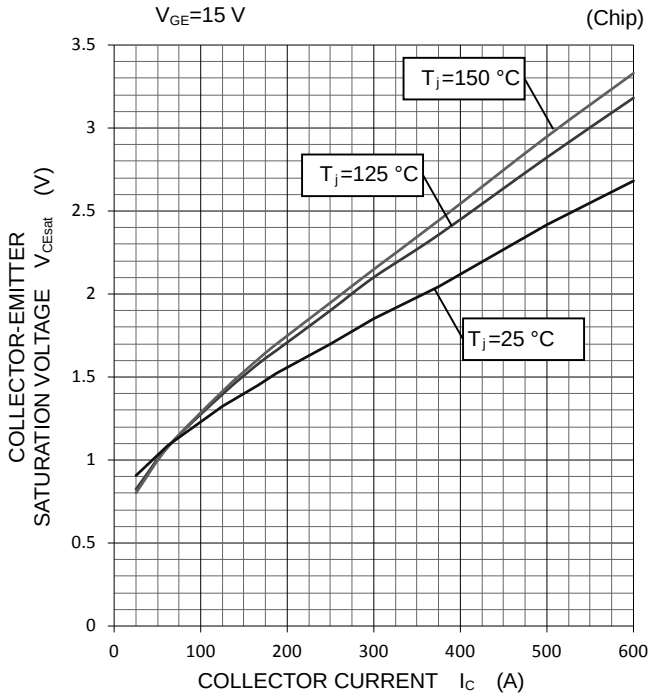
PERFORMANCE CURVES

INVERTER PART

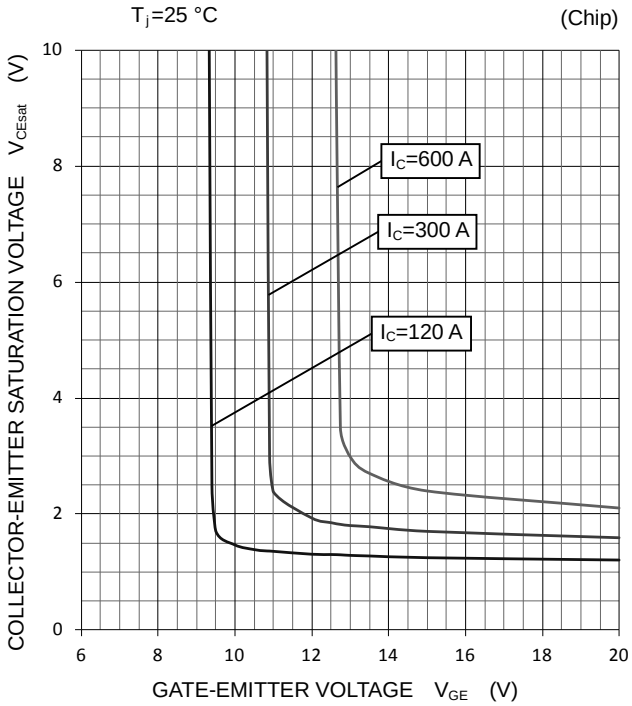
OUTPUT CHARACTERISTICS (TYPICAL)



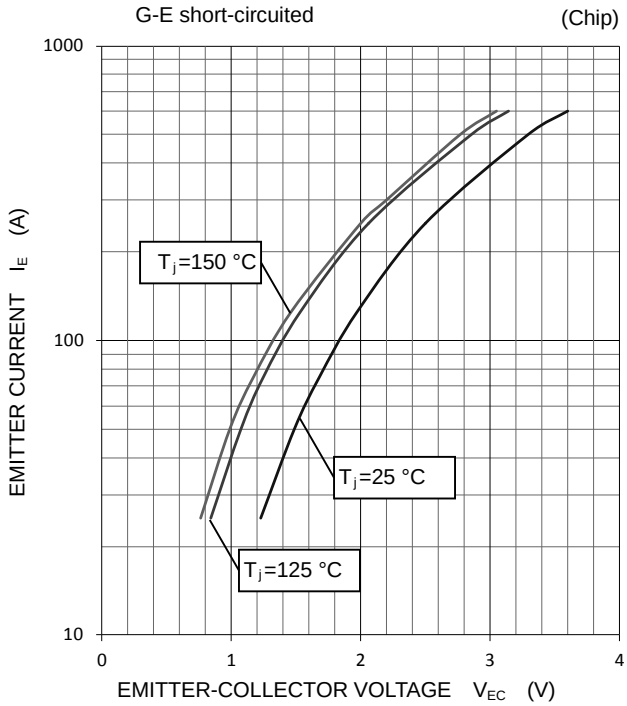
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)

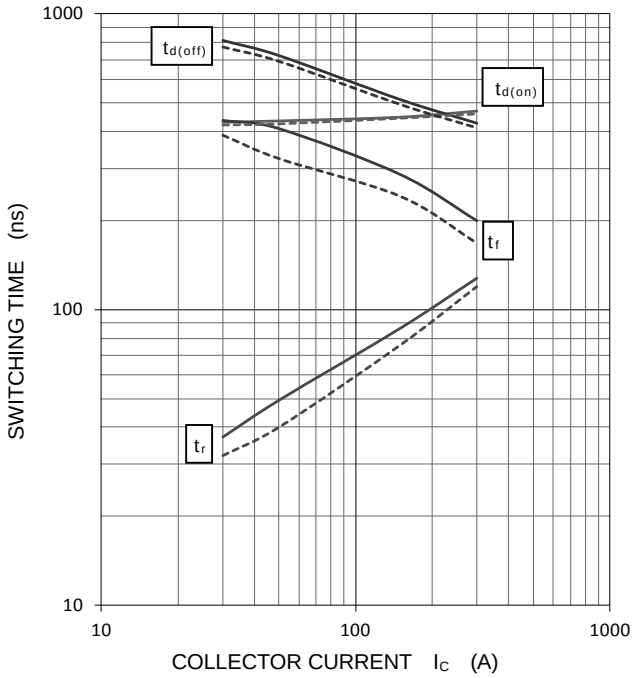


PERFORMANCE CURVES

INVERTER PART

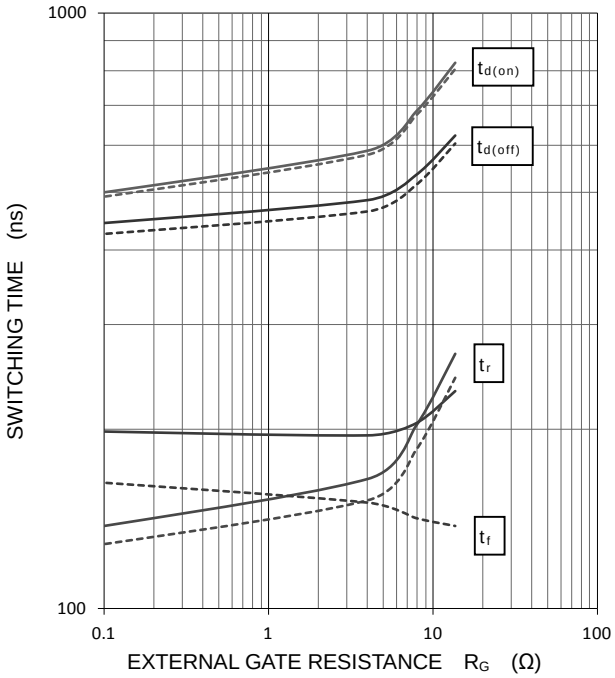
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=600 V, V_{GE}=±15 V, R_G=0 Ω, INDUCTIVE LOAD
———: T_j=150 °C, - - - - -: T_j=125 °C



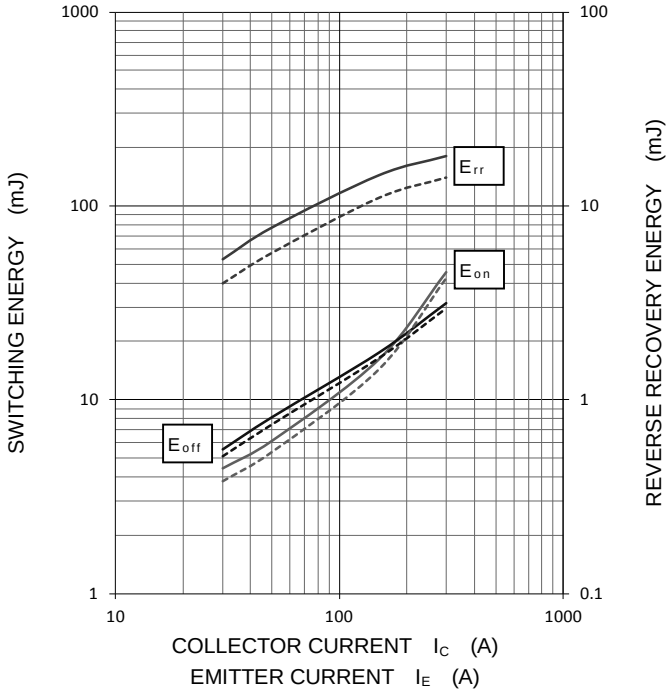
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=600 V, V_{GE}=±15 V, I_C=300 A, INDUCTIVE LOAD
———: T_j=150 °C, - - - - -: T_j=125 °C



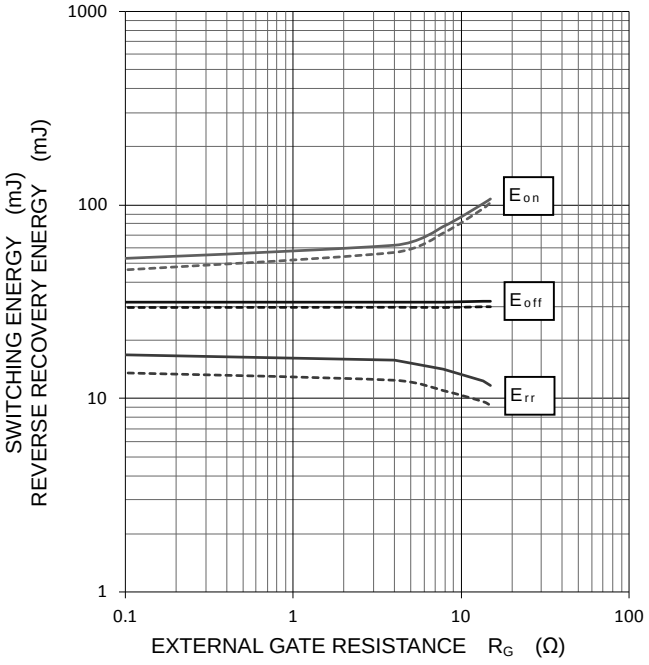
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=600 V, V_{GE}=±15 V, R_G=0 Ω,
INDUCTIVE LOAD, PER PULSE
———: T_j=150 °C, - - - - -: T_j=125 °C



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=600 V, V_{GE}=±15 V, I_C/I_E=300 A,
INDUCTIVE LOAD, PER PULSE
———: T_j=150 °C, - - - - -: T_j=125 °C

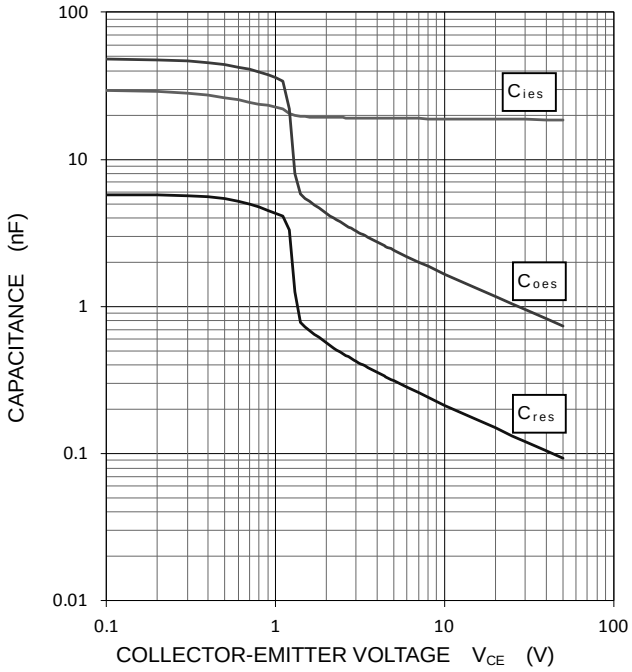


PERFORMANCE CURVES

INVERTER PART

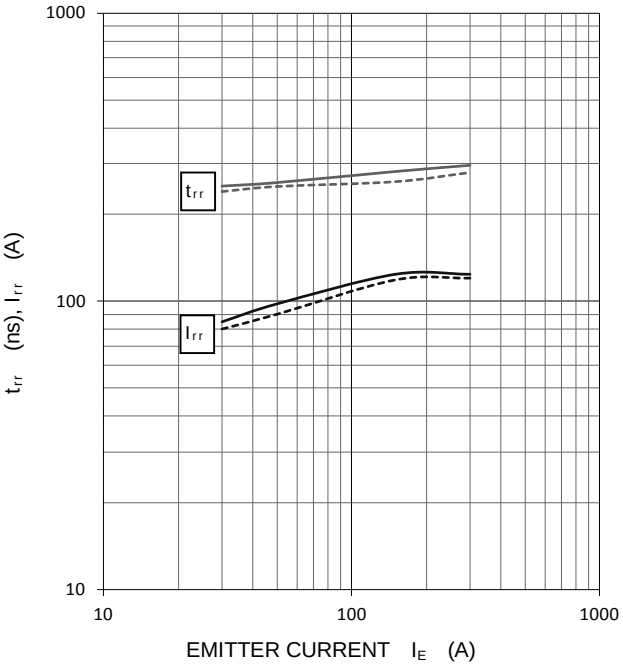
CAPACITANCE CHARACTERISTICS (TYPICAL)

G-E short-circuited, T_j=25 °C



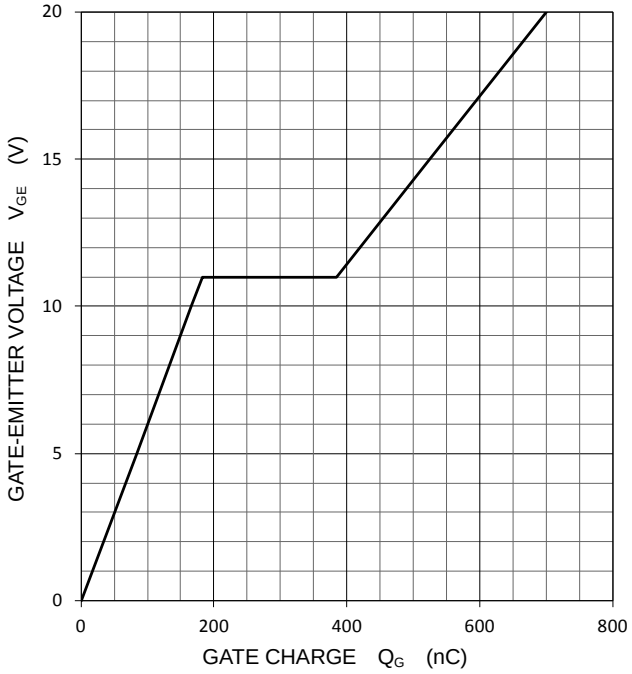
FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)

V_{CC}=600 V, V_{GE}=±15 V, R_G=0 Ω, INDUCTIVE LOAD
———: T_j=150 °C, - - - - -: T_j=125 °C



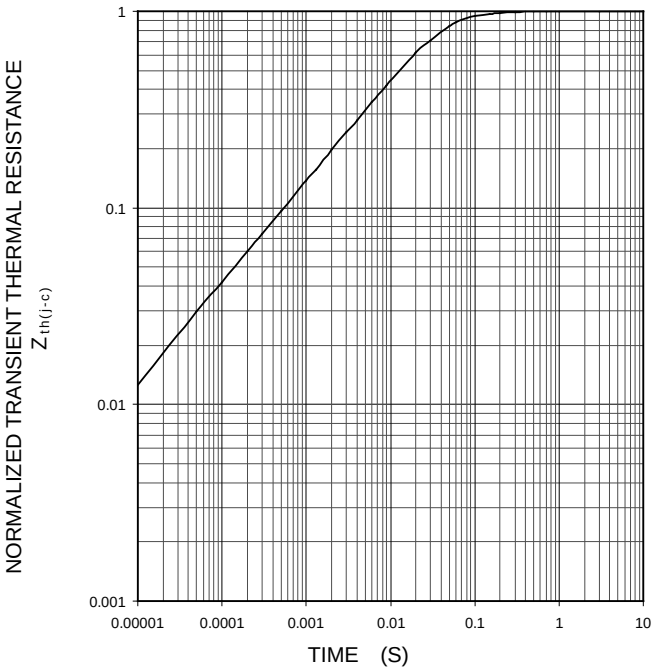
GATE CHARGE CHARACTERISTICS (TYPICAL)

V_{CC}=600 V, I_C=300 A, T_j=25 °C



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

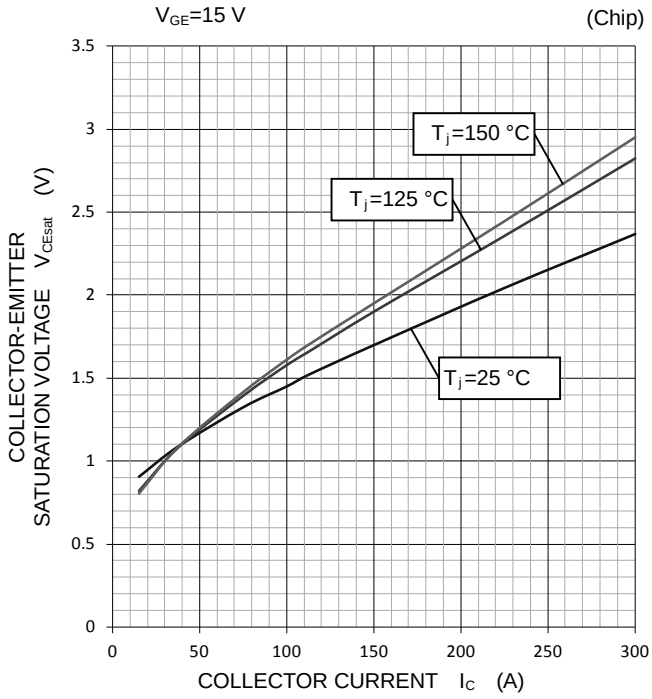
Single pulse, T_C=25 °C
$R_{th(j-c)Q}$=0.09 K/W, $R_{th(j-c)D}$=0.15 K/W



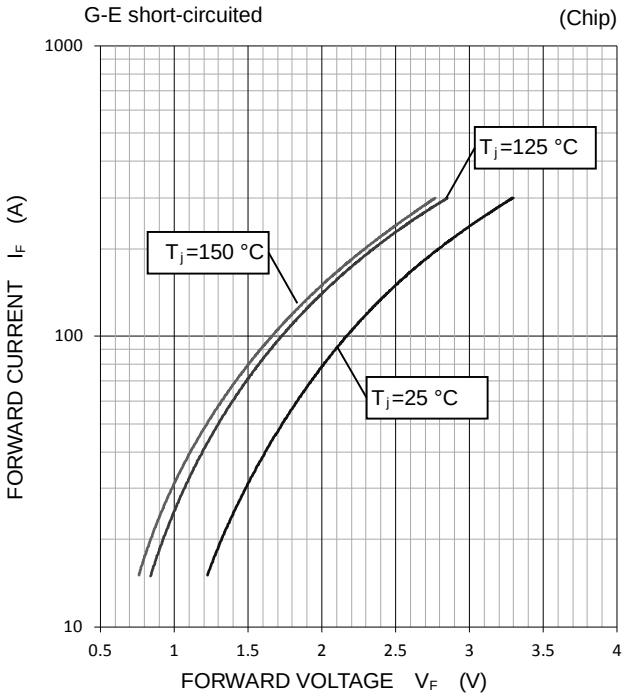
PERFORMANCE CURVES

BRAKE PART

COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)

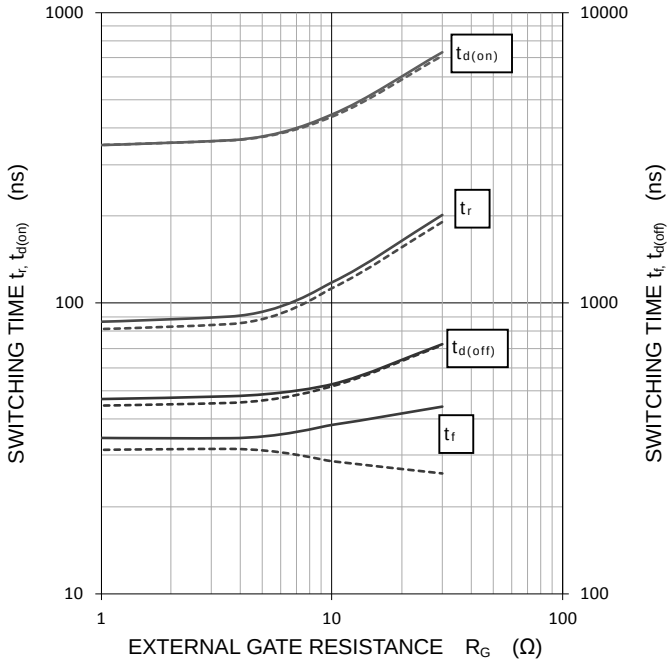


DIODE FORWARD CHARACTERISTICS (TYPICAL)



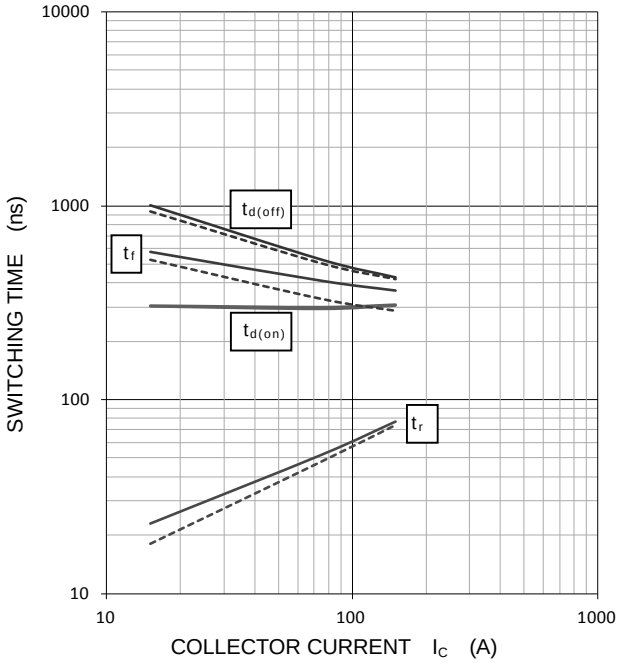
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=600 V, V_{GE}=±15 V, I_C=150 A,, INDUCTIVE LOAD
———: T_j=150 °C, - - - - -: T_j=125 °C



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=600 V, V_{GE}=±15 V, R_G=0 Ω, INDUCTIVE LOAD
———: T_j=150 °C, - - - - -: T_j=125 °C

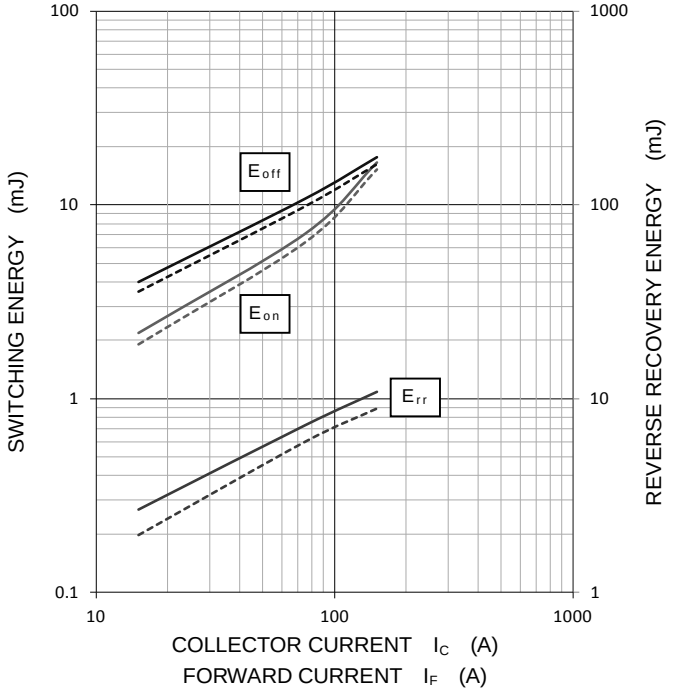


PERFORMANCE CURVES

BRAKE PART

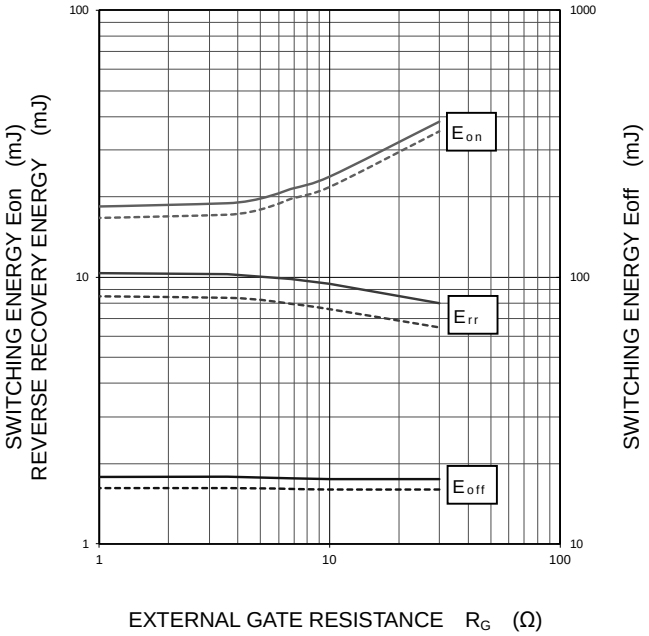
HALF-BRIDGE
SWITCHING CHARACTERISTICS
(TYPICAL)

V_{CC}=600 V, V_{GE}=±15 V, R_G=0 Ω,
INDUCTIVE LOAD, PER PULSE
———: T_j=150 °C, - - - - -: T_j=125 °C



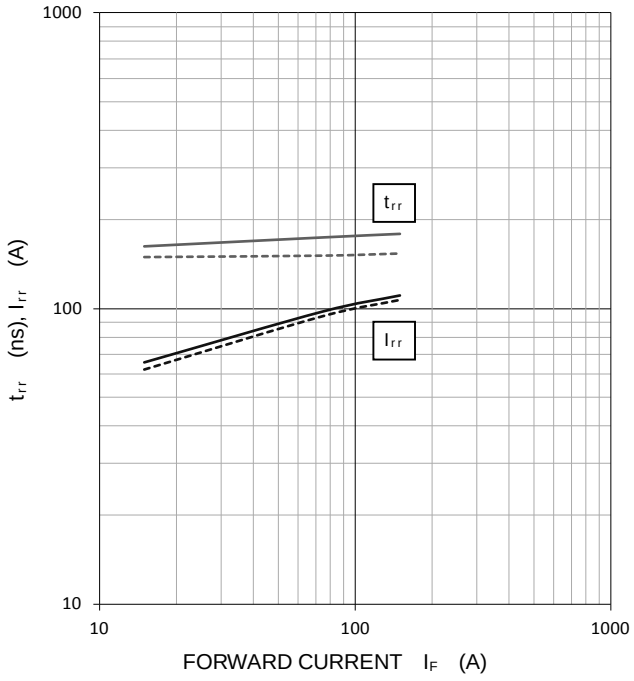
HALF-BRIDGE
SWITCHING CHARACTERISTICS
(TYPICAL)

V_{CC}=600 V, I_C/I_F=150 A, V_{GE}=±15 V,
INDUCTIVE LOAD, PER PULSE
———: T_j=150 °C, - - - - -: T_j=125 °C



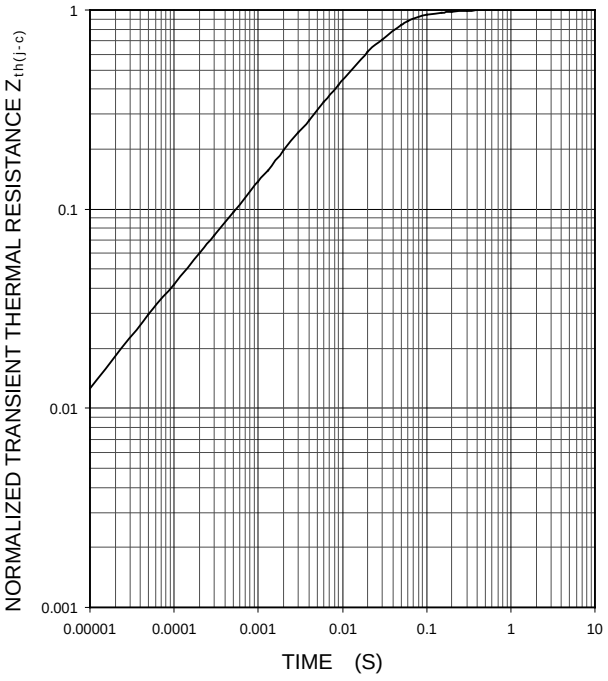
DIODE
REVERSE RECOVERY CHARACTERISTICS
(TYPICAL)

V_{CC}=600 V, V_{GE}=±15 V, R_G=0 Ω, INDUCTIVE LOAD
———: T_j=150 °C, - - - - -: T_j=125 °C



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS
(MAXIMUM)

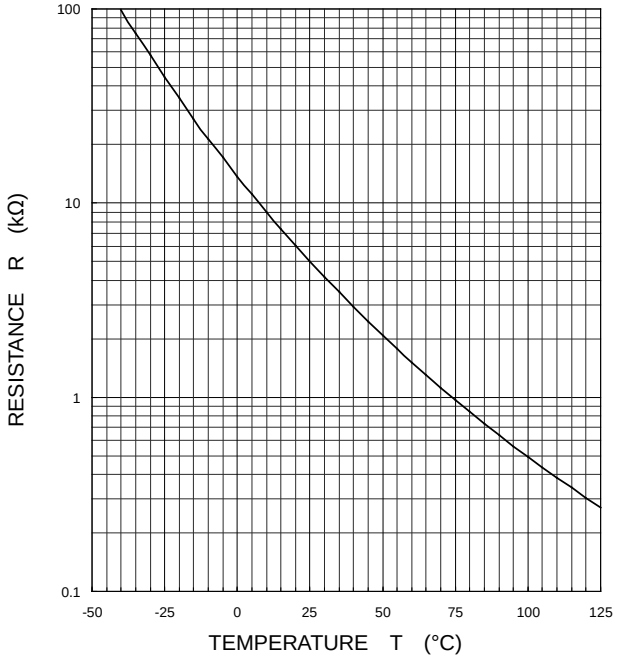
Single pulse, T_C=25 °C
$R_{th(j-c)Q}$=0.16 K/W, $R_{th(j-c)D}$=0.26 K/W



PERFORMANCE CURVES

NTC thermistor part

TEMPERATURE CHARACTERISTICS
(TYPICAL)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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